Chng to some 511, 877

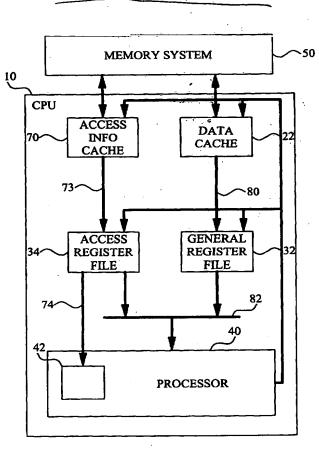# (12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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(54) Title: MEMORY ACCESS REGISTER FILE

(57) Abstract: The general idea according to the invention is to introduce a special-purpose register file (34) adapted for holding memory address calculation information received from memory (50, 70) and to provide one or more dedicated interfaces (73, 74) for allowing efficient transfer of memory address calculation information in relation to the special-purpose register file. The special-purpose register file (34) is preferably connected to at least one functional processor unit (42), which is operable for determining a memory address based on memory address calculation information received from the special-purpose register file (34). Once the memory address has been determined, the corresponding memory access can be effectuated.

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